

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Priority Application Serial No
Priority Filing Date September 1, 1999
Inventor Luan C. Trar
Assignee Micron Technology, Inc
Priority Group Art Unit
Priority Examiner L. Schillinge
Attorney's Docket No
Title: Semiconductor Processing Methods of Forming Integrated Circuitry

Assistant Commissioner for Patents Washington, D.C. 20231

ASSOCIATE POWER OF ATTORNEY

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Date: 4/30/01

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